



Spread Spectrum Clock Generator AK8126B

Features

- **Output Frequency Range:**
90MHz – 128MHz
1X or
Convert 27MHz to 100MHz (3.7X)
- **Configurable Spread Spectrum Modulation:**
 - **AKEMD's Original Spread Spectrum Profile**
 - **Modulation Ratio:**
 - Center Spread:**
±0.25%, ±0.5%, ±1.0%, ±1.5%
 - Down Spread:**
-0.5%, -1.0%, -2.0%, -3.0%
 - **Modulation Frequency**
27.04KHz – 38.46KHz
- **Input Frequency Range:**
 - **Crystal: 27.000MHz**
 - **Reference Clock Input : 90MHz – 128MHz**
- **Low Jitter Performance:**
60 ps (Max.) cycle to cycle jitter 1s
200 ps(Max) cycle to cycle jitter peak
- **Low Current Consumption:**
4.5mA (Typ.) at 100MHz, 3.3V
- **Supply Voltage:**
3.0 – 3.6V
- **Operating Temperature Range:**
-10 to +85°C
- **Package:**
10-pin TMSOP (Thin Miniature SOP)
Lead Free & Halogen Free

Description

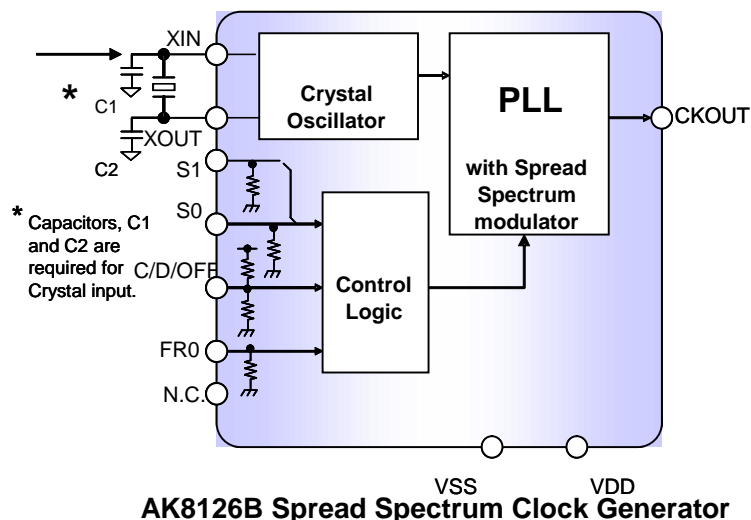
The AK8126B is a spread spectrum clock generator designed for Home AV applications where EMI elimination is seriously required. The device uses the AKEMD's original spread spectrum profile** to provide excellent EMI reduction effect without degradation of system performance. The AK8126B accepts a wide clock frequency range from a crystal or reference clock input, and provides a variable spread spectrum modulation selectable by pin-setting, allowing the device to use in most AV products easily. The device is available in a very small 10-pin TMSOP package, and its pin configuration offers simple PCB layout.

** : Patent pending

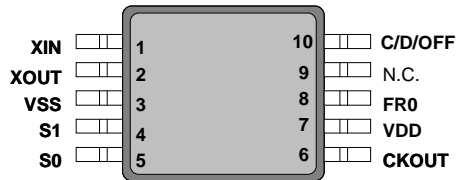
Applications

- Digital TV Sets
- Display Panels
- Personal Video Recorders
- Set-Top-Boxes
- Multi Media Receivers
- PC graphics board

Block Diagram



Pin Descriptions



Package: 10-Pin TMSOP (Top View)

Pin No.	Pin Name	Pin Type	Description
1	XIN	IN	Crystal Input or Reference Clock Input
2	XOUT	OUT	Crystal Output. Leave this pin floating when the external clock is used.
3	VSS	--	Ground
4	S1	IN	Spread Spectrum Modulation Ratio Select Pin1 (1)
5	S0	IN	Spread Spectrum Modulation Ratio Select Pin0 (1)
6	CKOUT	OUT	Clock Output
7	VDD	--	Power Supply
8	FR0	IN	Clock Frequency Range Select Pin 0 (1)
9	N.C.	NC	Connect to VDD or GND (2)
10	C/D/OFF	IN	Spread Spectrum Modulation Mode Select Pin (3)

(1) Internal pull down 360kΩ (Typ.)

(2) Internal pull up 360kΩ (Typ.)

(3) 3-Level Input for Low/Mid/Hi. Biased at 1/2 of VDD with pull-up/down resistors of 360kΩ (Typ.).

Ordering Information

Part Number	Marking	Shipping Packaging	Package	Temperature Range
AK8126B	126B	Tape and Reel	10-pin TMSOP	-10 to 85 °C

Absolute Maximum Rating

Over operating free-air temperature range unless otherwise noted ⁽¹⁾

Items	Symbol	Ratings	Unit
Supply Voltage	VDD	-0.3 to 4.6	V
Input Voltage	V _{in}	VSS-0.3 to VDD+0.3	V
Input Current (any pins except supplies)	I _{IN}	±10	mA
Storage Temperature	T _{stg}	-55 to 130	°C

Note

(1) Stress beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only. Functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to absolute-maximum-rating conditions for extended periods may affect device reliability. Electrical parameters are guaranteed only over the recommended operating temperature range.



ESD Sensitive Device

This device is manufactured on a CMOS process, therefore, generically susceptible to damage by excessive static voltage. Failure to observe proper handling and installation procedures can cause damage. AKEMD recommends that this device is handled with appropriate precautions.

Recommended Operation Conditions

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Operating Temperature	T _a		-10		85	°C
Supply Voltage	VDD		3.0	3.3	3.6	V
Input Clock Frequency ⁽¹⁾	Fin 1	Crystal	24.3	27	32.4	MHz
	Fin 2	External Reference Clock Input Fin2 ≤ 100MHz Amplitude: More than 1Vpp Duty: 30%-70%	24.3	27	32.4	MHz
		Fin > 100MHz Amplitude: More than 1.2Vpp Duty: 40%-60%	90		128	MHz
Output Load Capacitance	Cp1	Output Frequency 90MHz to Less than 100MHz			10	pF
		Output Frequency 100MHz to 128MHz			8	pF

Note:

(1) Frequency range to operate is required to select by pin-setting of FR0. See Table for the frequency range selection.

DC Characteristics

All specifications at VDD: over 3.0 to 3.6V, Ta: -10 to +85°C, Output Frequency: over specified frequency, unless otherwise noted

Parameter	Symbol	Conditions	MIN	TYP	MAX	Unit
High Level Input Voltage 1	V _{IH 1}	Pin: S0,S1,FR0	0.8VDD			V
Low Level Input Voltage 1	V _{IL 1}	Pin: S0,S1,FR0			0.2VDD	V
High Level Input Voltage 2	V _{IH 2}	Pin: C/D/OFF	0.8VDD			V
Low Level Input Voltage 2	V _{IL 2}	Pin: C/D/OFF			0.15VDD	V
Mid Level Input Voltage 2	V _{IM 2}	Pin: C/D/OFF	0.45VDD	0.50VDD	0.55VDD	V
Input Current	I _L	Pin: S0, S1, FR0, C/D/OFF	-20		+20	μA
High Level Output Voltage	V _{OH}	Pin: CLKOUT I _{OH} =-4mA	0.8VDD			V
Low Level Output Voltage	V _{OL}	Pin: CLKOUT I _{OL} =+4mA			0.2VDD	V
Current Consumption	I _{DD}	No load, Ta=25°C Input frequency 100MHz Spread Spectrum Mode selection by note ⁽²⁾		4.5		mA

(2) In the mode of center spread ±1.0%

AC Characteristics

All specifications at VDD: over 3.0 to 3.6V, Ta: -10 to +85°C, Output Frequency: over specified frequency, unless otherwise noted

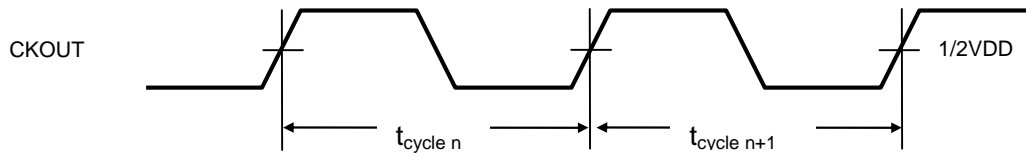
Parameter	Symbol	Conditions	MIN	TYP	MAX	Unit
Output Clock Duty Cycle ⁽⁴⁾			40	50	60	%
Output Clock Rise Time ⁽⁴⁾	t _{rise}			1.2		ns
Output Clock Fall Time ⁽⁴⁾	t _{fall}			1.2		ns
Output Clock Jitter	Jit 1	Cycle-to-cycle 1σ, 1x,3.7X @100MHz 1000 cycles			60	ps
	Jit 2	Cycle-to-cycle Peak, 1x,3.7X @100MHz 10000 cycles			200	ps
Output Lock Time ⁽³⁾	t _{lock}	Power-up		5		ms

(3) The time that output reaches the target frequency within accuracy of ±0.1% from the point that the power supply reaches VDD

(4) With the load capacitance specified by the recommended operation conditions

Definition of Jitters

1. Cycle to cycle jitter: The variation in cycle time of a single between adjacent cycles, over a random sample of adjacent cycle pairs.



$CCJ = | t_{\text{cyclen}} - t_{\text{cyclen+1}} |$: where $t_{\text{cycle } n}$ and $t_{\text{cyclen+1}}$ are any two adjacent cycles measured on controlled edges.

Spread Spectrum Modulation Characteristics

All specifications at VDD: over 3.0 to 3.6V, Ta: -10 to +85°C, Output Frequency: over specified frequency, unless otherwise noted

Parameter	Symbol	Conditions	MIN	TYP	MAX	Unit
Modulation Frequency ⁽⁵⁾	f_{mod}		27.04		38.46	kHz
Modulation Ratio @ Center Spread	Mod C	Ratio Selection Pin S[1:0]=LH		±0.25		%
		Ratio Selection Pin S[1:0]=LL		±0.5		
		Ratio Selection Pin S[1:0]=HL		±1.0		
		Ratio Selection Pin S[1:0]=HH		±1.5		
Modulation Ratio @ Down Spread	Mod D	Ratio Selection Pin S[1:0]=LH		-0.5		%
		Ratio Selection Pin S[1:0]=LL		-1.0		
		Ratio Selection Pin S[1:0]=HL		-2.0		
		Ratio Selection Pin S[1:0]=HH		-3.0		
Average Modulation Frequency Offset ⁽⁶⁾ @ Down Spread	$f_{mod-off}$	Ratio Selection Pin S[1:0]=LH		-0.36		%
		Ratio Selection Pin S[1:0]=LL		-0.6		
		Ratio Selection Pin S[1:0]=HL		-1.2		
		Ratio Selection Pin S[1:0]=HH		-1.8		
Average Modulation Frequency Offset ⁽⁶⁾ @ Down Spread 27MHz to 100MHz mode	$f_{mod-off}$	Ratio Selection Pin S[1:0]=LH		-0.375		%
		Ratio Selection Pin S[1:0]=LL		-0.625		
		Ratio Selection Pin S[1:0]=HL		-1.25		
		Ratio Selection Pin S[1:0]=HH		-1.875		

(5) Modulation frequency is determined by input frequency range. Following equations provide the theoretical modulation frequency of AK8126B.

Output Frequency Range 90-128MHz: $f_{mod}(KHz) = 19.23KHz \times f_{in}(MHz) / 64MHz$

27MHz to 100MHz convert mode: $f_{mod}(KHz) = 31.25kHz @ 100MHz$

Where, f_{in} is an input frequency of crystal or reference clock input.

(6) The average output frequency in the mode of down spread modulation is intentionally offset against the ideal average frequency. The offset is specified by percentage to the target output frequency as shown in **figure 1**.

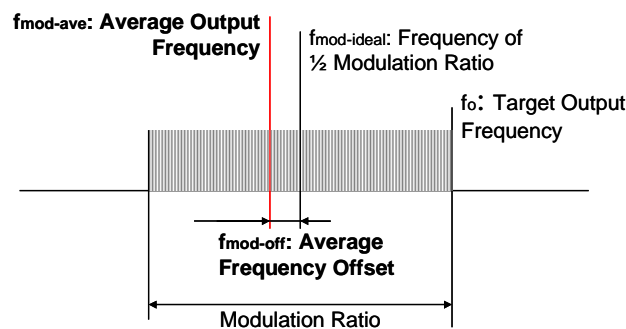


Figure 1: Offset in the mode of down spread modulation

Operation Clock Frequency Range Selection

The AK8126B operates at a wide frequency range to support clocking in most of Home AV products. The operation frequency range applying to the AK8126B is required to select by pin-setting of FR0 (Pin8), as shown in **Table 1**.

Table 1: Operation Clock Frequency Setting

Pin Setting FR0	Input Frequency (MHz)	Output Frequency (MHz)	Input Frequency Scaling	Applicable Input	
				Crystal	Clock
L	90 – 128	90 – 128			ū
H	24.3 – 32.4	90 – 120	X3.7	ū	ū

Spread Spectrum Modulation Selection

The AK8126B operates in two modes of spread spectrum modulation. One is “Center Spread Modulation” in which the frequency deviation from the target output frequency is equal to the lower side and the upper side. The other one is “Down Spread Modulation”, which will be used in applications where the target clock is providing at a maximum frequency rate of that system. In this mode, the maximum spread frequency is equal to the target frequency, and the frequency deviation is provided in the lower side. The AK8126B can provide a variable modulation ratio with the AKEMD's original spread profile for both modulation modes, offering appropriate tune for EMI elimination without degradation of system performance.

The modulation mode and the modulation ratio are selectable by pin setting of C/D/OFF (Pin 10) and S[1:0] (Pin 4 and 5), as defined in **Table 2** and **Table 3**, respectively.

Table 2: Spread Spectrum Modulation Mode Setting

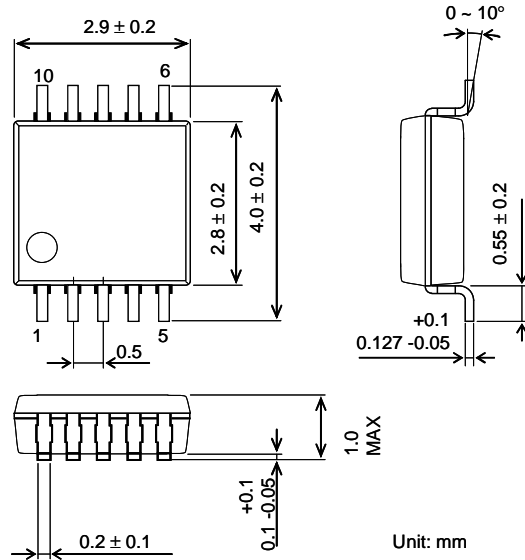
Pin Setting C/D/OFF	Modulation Mode
L	Down Spread
M	No Modulation
H	Center Spread

Table 3: Modulation Ratio Setting

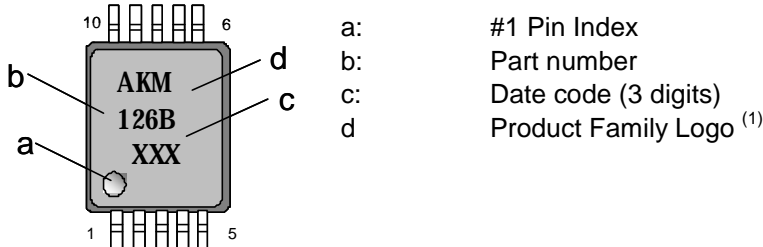
Pin Setting		Modulation Ratio (%)	
S1	S0	Center Spread Mode	Down Spread Mode
L	L	± 0.5	-1.0
L	H	± 0.25	- 0.5
H	L	± 1.0	-2.0
H	H	± 1.5	- 3.0

Package Information


• Mechanical data



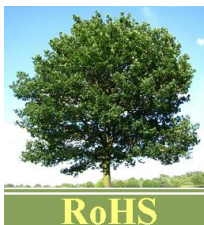
• Marking



(1) **AKM** is the brand name of AKEMD's IC's.

AKM and the logo -  - are the brand of AKEMD's IC's and identify that AKEMD continues to offer the best choice for high performance mixed-signal solution under this brand.

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(*) RoHS compliant products from AKEMD are identified with "Pb free" letter indication on product label posted on the anti-shield bag and boxes.

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