



**-Preliminary-**

# AKD8140A Ver.2

## Evaluation Board for AK8140A

### 1. Discription

AKD8140A Ver.2 is an evaluation board for AK8140A Programmable Multi Clock Generator.

It is ease to evaluation Jitter performances and functions.

### 2. Ordering guide

AKD8140A Ver.2

### 3. Block Diagram

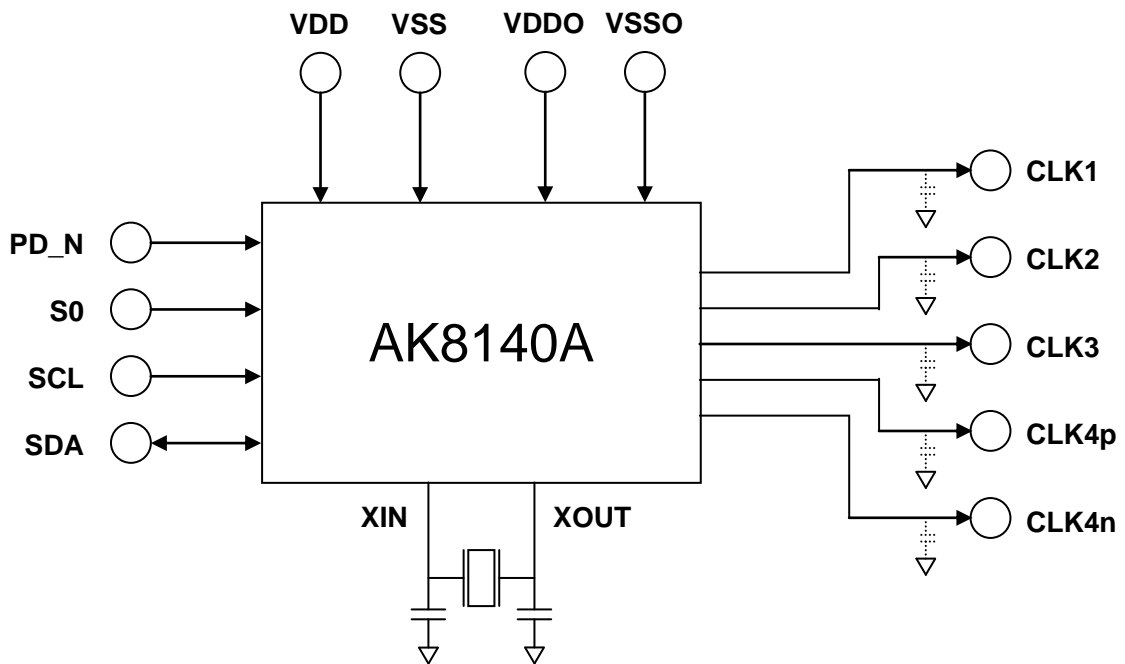


Figure 1 : AKD8140A Block Diagram

## 4. AKD8140A Ver.2 Evaluation Board

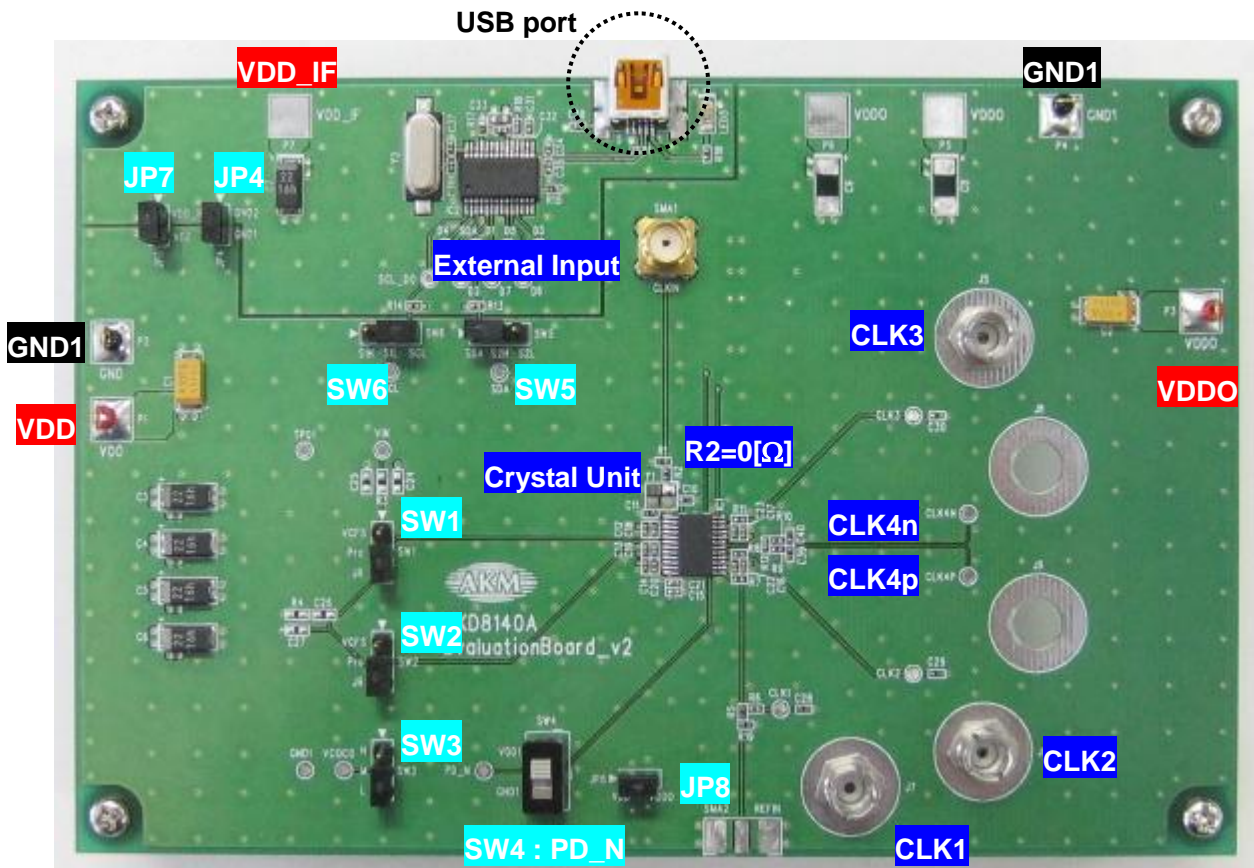


Figure 2 : AKD8140A Ver.2 Evaluation Board

### 1) Power Supply

VDD = VDD1 = VDD2 = VDD3 = VDD4, VDDO = VDDO1 = VDDO2

[JP4] Short GND1 and GND2

[JP7] Short VDD and VDD\_IF

[JP8] Short VDD and VDDO, when VDD=VDDO=3.3V.

Open VDD and VDDO, when VDD=3.3V and VDDO=1.8V.

### 2) SW Setting

[SW1] Short Pro and JR

[SW2] Short Pro and JR

[SW3] Short M and L, so GND pin and S0 pin is connected to GND.

[SW4] SW4 = GND1 is PD\_N pin = "L", when AK8140A is power down.

SW4 = VDD1 is PD\_N pin = "H", when AK8140A is operating.

## AKD8140A

### 3) Clock Input

The clock input is selectable to provide from External Clock or Crystal Unit.

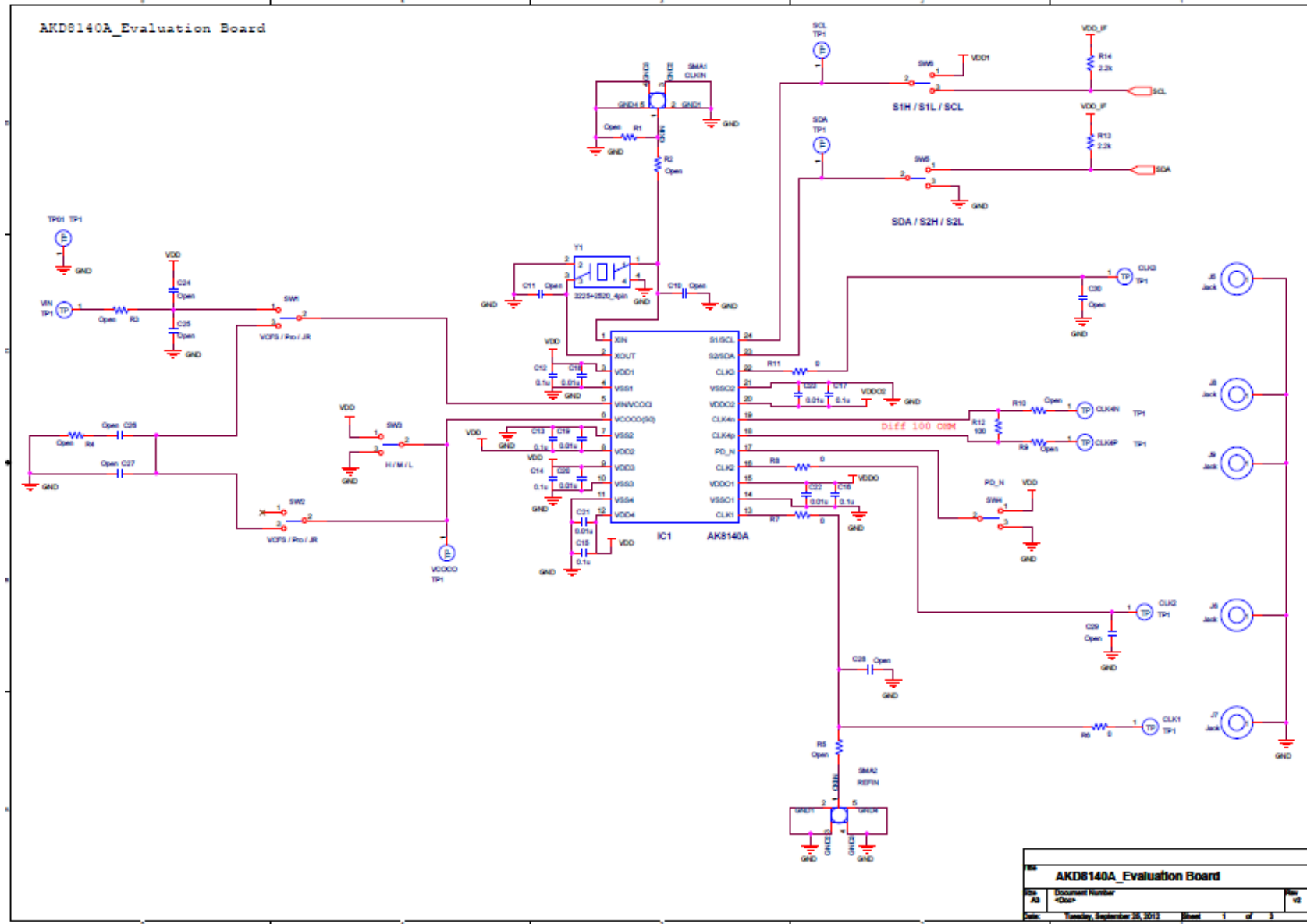
[External Input] Mount 0[Ω] register in R2, when External Clock Input is connected to AK8140A XIN pin.

[Crystal Unit] Mount 3225 size or 2520 size Crystal Unit, when Crystal Unit is connected to AK8140A XIN pin and XOUT pin. Don't mount 0[Ω] register in R2.

### 4) USB port

Register of AKD8140 is written from PC through USB cable (USB A - miniB).

[USB port] USB cable can be connected.



# AKD8140A

